## **Agenda**

- 13:15 13:45 Light lunch, welcome and registration
- 13:45 14:00 Brief presentation of the WorkShop
- 14:00 14:15 General Presentation of the SMARTNET project (RSE)
- 14:15 14:45 Italian Regulatory framework presentation and the implemented services (Terna)
- 14:45 15:00 Pilot A DSO's network details (Edyna)
- 15:00 15:20 HVRS High Voltage Regulation System for the Hierarchycal voltage coordination (Terna)
- 15:20 15:35 Coffee break
- 15:35 15:55 MVRS (1) Medium Voltage Regulation System equipment for MV voltage regulation, aFRR activation and aggregated representation of the resources (SELTA)
- 15:55 16:15 MVRS (2) Medium Voltage Regulation System equipment for MV voltage regulation, aFRR activation and aggregated representation of the resources (SIEMENS)
- 16:15 16:30 Conclusions (Terna, all)
- 16:30 17:30 Q&A (all)